

Biography

WILLIAM S. SONG is a senior staff member in the Embedded Digital Systems Group at MIT Lincoln Laboratory. He received his B.S., M.S., and Ph.D. degrees from the Massachusetts Institute of Technology in 1982, 1984, and 1989, respectively.

Since his arrival at Lincoln Laboratory in 1990, Dr. Song has been working on high-performance sensor and VLSI signal processor technologies for adaptive sensor array applications. He has developed numerous advanced signal processing algorithms, architectures, real-time embedded processors, and sensor array systems. Recently, he has been working on the nonlinear equalization processor, mixed-signal system on chip, high-throughput low-power VLSI signal processors, and highly digitized wideband sensor arrays.

Dr. Song has been the technical director for a number of programs, including the receiver-on-chip, wideband linearity enhancement processor, X-band element-level digitized sensor array, space-based radar onboard signal processor, high-dynamic-range digital receiver, adaptive digital beamformer processor, and miniaturized mixed-signal receiver/processor programs. He has also designed a series of high-performance special-purpose signal processor ICs for these applications. His accomplishments include six U.S. patents, 17 invention disclosures, and 18 publications. He is also an IEEE Senior Member.