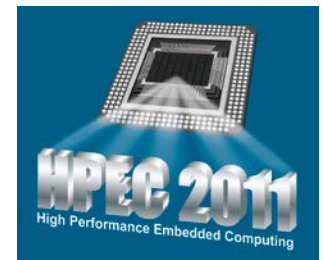


# Session 3: Silicon

**Brian Sroka / The MITRE Corporation**

**HPEC Conference**

**22 September 2011**





## Session 3: Silicon Agenda

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- 0915      **Creating Chip Generators for Efficient Computing at Low NRE Design Costs**  
Ofer Shacham / Stanford University, Chip Genesis Inc.
- 0945      **The Dark Silicon Implications for Microprocessors**  
Karu Sankaralingam / University of Wisconsin
- 1015      **Self-assembled Materials for Computer Architectures**  
Chris Dwyer / Duke University
- 1045      **Break**
- 1100      **High-Dynamic-Range, Real-Time Architecture for Robust Space-Time Synchronization Algorithm**  
Vitaliy Gleyzer, Tom Baillio and Adam Margetts / MIT Lincoln Laboratory
- 1130      **Toward the Embedded Petascale Architecture: The Dual Roles of Middleware and Software Engineering in Future HPEC**  
Anthony Skjellum / University of Alabama at Birmingham

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# Poster / Demo B: Technologies and Systems

Brian Sroka / The MITRE Corporation

HPEC Workshop

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