

HPPEC 2011

High Performance Embedded Computing

HPEC 2011 Workshop Opening Remarks

Mr. Robert Bond

21 September 2011





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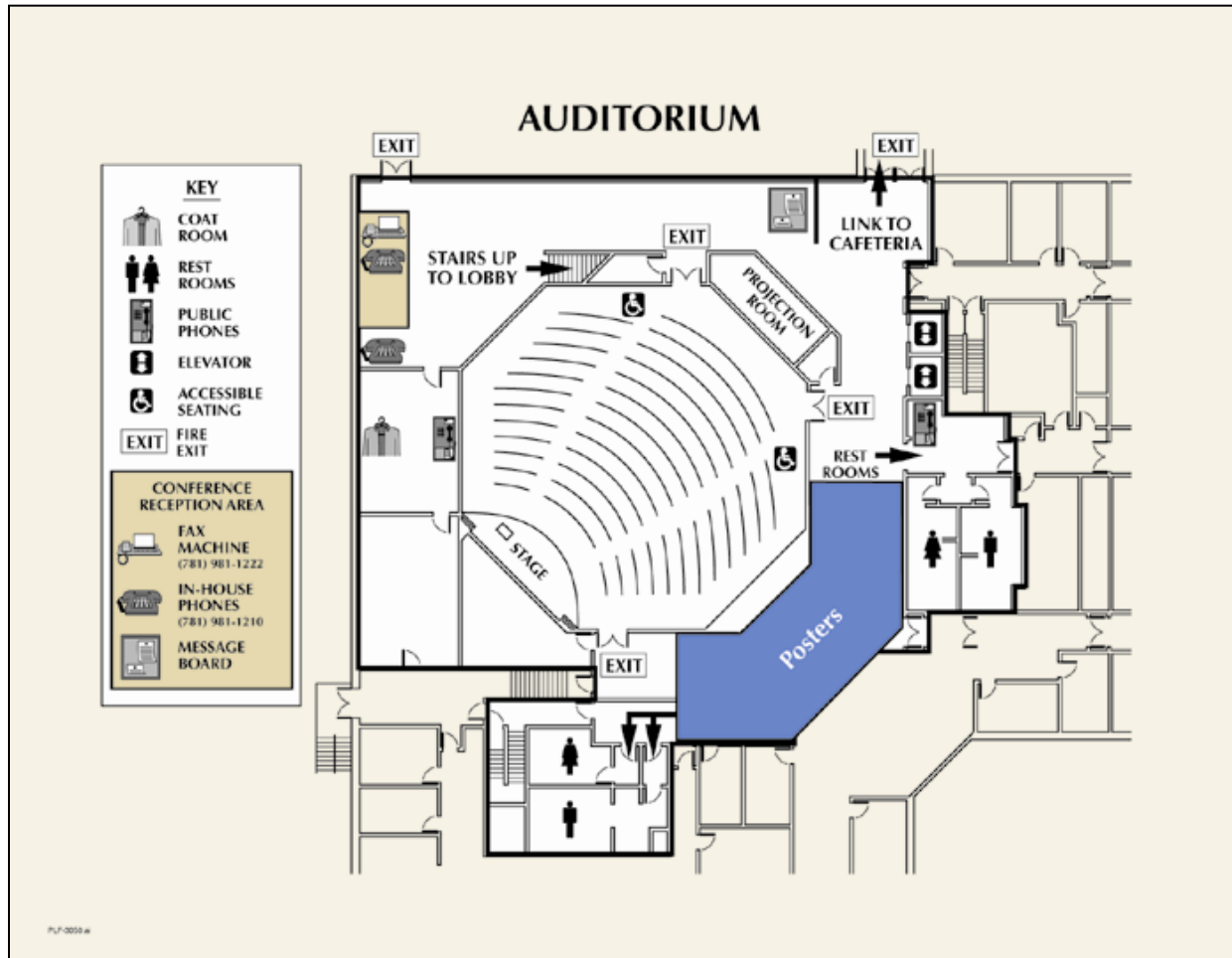
2011 HPEC Workshop

Administrative Information

- **Unclassified Sessions and a U.S. Only Session**
- **Workshop is being videotaped**
- **No smoking inside the Laboratory**
- **Lunch and refreshments are provided**
- **Wireless devices that include embedded cameras or recording devices are restricted. Please see the Registration Desk for storage**
- **Badges**
 - **100% Picture ID check is required each day**
 - **Picked up at the Security Desk each morning and returned before you leave**
 - **May not be taken outside the facility**
- **Unclassified Proceedings will be available on the web**
- **Please provide any necessary updates on the Contact Information form that is provided in your Attendee Package and leave at the Registration Desk**
- **Please provide your completed Workshop questionnaire to the Registration Desk**



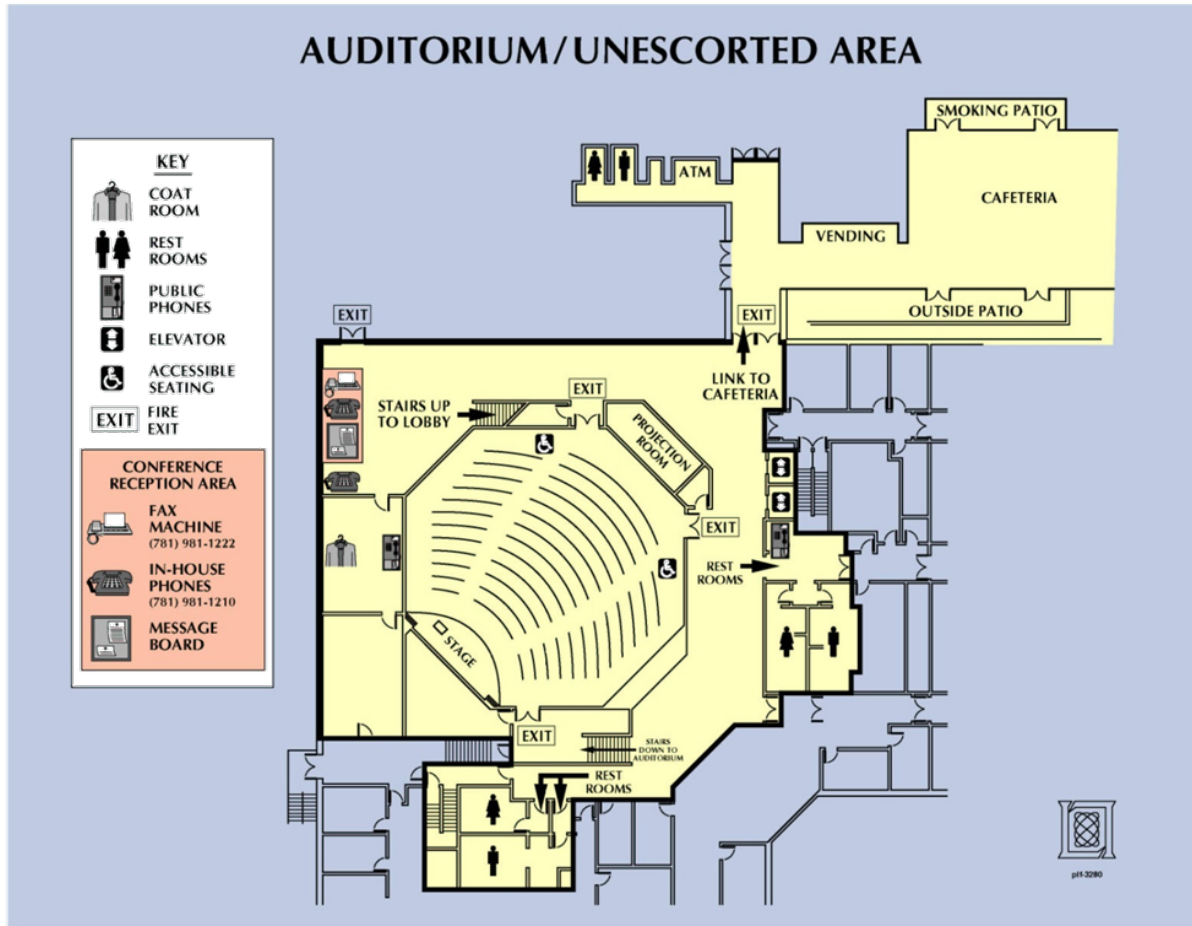
Auditorium



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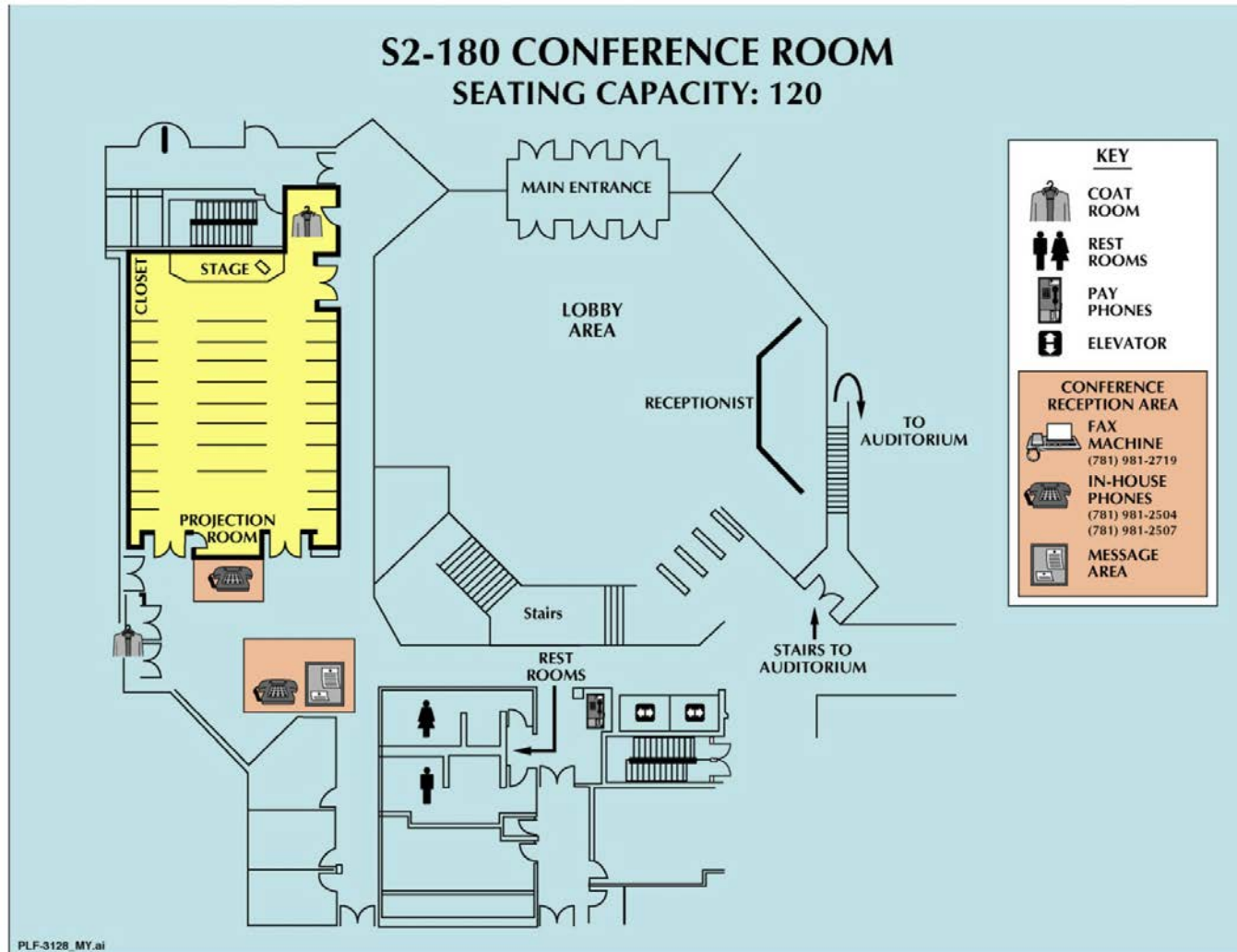
Unescorted Area



Lincoln Laboratory is a secure facility. Access to any other portion of the facility requires continuous escort by a white-badged employee.



Focus Sessions in S2-180





Agenda Highlights

Day 1
Wednesday
21 September

Mission Keynote Address – Dr. Mark Maybury / U.S. Air Force

Technology Keynote Address – Prof. Anant Agarwal / MIT CSAIL

Session 1: *Embedded Exascale Processing* Focus 1: *Open Systems*

Poster / Demo A: *Research and Applications*

Session 2: *Graph and Network Processing* Focus 2: *Secure and Power Efficient Computing*

Reception and Banquet (Burlington Marriott)

Banquet Presentation – Dr. Thomas Sterling / Indiana University

Title: The HPC Paradigm Shift 2020: HPEC Victim or Victor?

Day 2
Thursday
22 September

2012 Prenote – Mr. John Goodhue / Massachusetts Green High Performance Computing Center

Session 3: *Silicon* Focus 3: *GPU*

Poster / Demo B: *Technologies and Systems*

Session 4: Awards Session

Panel: *Will Security Impact Using COTS for HPEC*



Banquet Speaker

Thomas Sterling, Ph.D

Title: The HPC Paradigm Shift 2020: HPEC Victim or Victor?

Place: Burlington Marriott

Date: Wednesday, 21 Sept 2011

Time: 1800 – Reception
1830 – Speaker
1900 – Banquet





IEEE HPEC

High Performance Extreme Computing

Charter: “To serve as the premier conference in the world on the confluence of HPC and Embedded Computing”

Goals

- Migrate core of HPEC Workshop to an annual New England-focused IEEE conference
- Foster fuller participation by academic and industrial community
- Expand HPEC to include emerging Holyoke supercomputing community

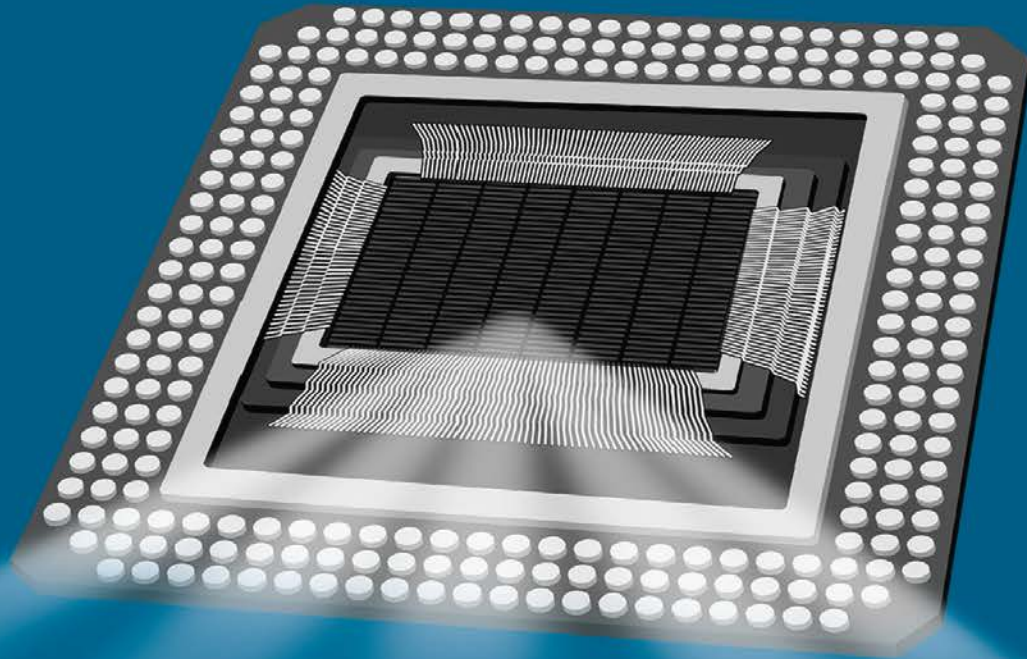
Program

- 11-12 September 2012, Westin in Waltham
- Academic track and demo night

Sub-Topics

- Architectures
- Data Intensive Computing
- Embedded & Power Efficient Computing
- Cyber Security
- HPC (Power Efficient & Green)
- Massive Databases
- Convergence of mobile/cloud computing





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