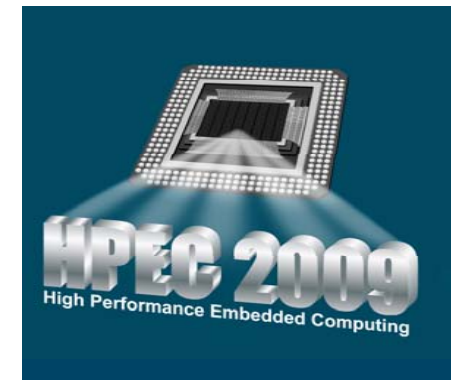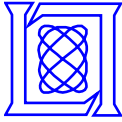# Session 2:
Decision Support

Nadya Bliss / MIT Lincoln Laboratory

HPEC Conference

22 September 2009

MIT Lincoln Laboratory

HPEC and Decision Support

SYSTEM

APPLICATIONS

ALGORITHM/ARCHITECTURE OPTIMIZATION & MAPPING

HARDWARE ARCHITECTURES

1345 **Invited: Cross-Domain ISR Maritime Awareness Demonstration**
Kenneth Gregson / MIT Lincoln Laboratory

1415 **Stochastic Digital Circuits for Probabilistic Inference**
Vikash Mansinghka / Massachusetts Institute of Technology

1445 **Multi-objective Optimization of Sparse Array Computations**
Una-May O'Reilly / Massachusetts Institute of Technology

1515 **Break** (View Posters)

1530 **3D Exploitation of Large 2D Urban Photo Archives**
Peter Cho / MIT Lincoln Laboratory

1600 **Optimizing an Innovative SAR Post-Processing Algorithm for Multi-Core Processors: A Case Study**
Peter Carlston / Intel Corporation

1630 **Effective Floating Point Applications on FPGAs: Examples from Molecular Modeling**
Martin Herbordt / Boston University