LabVIEW Real Time for High Performance Control Applications

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Case studies: Matrix-vector Multiplication & PDE Tool Most of the basic computational algorithms for high performance computing are designed to have the best average performance for the most general case as they are used in off-line calculations: the goal is for calculation to end in (Non)Linear Elliptic PDE Solver LabVIEW Graphical Development Environment as little time as possible for an arbitrary set of inputs but each step in general does not have a strict time deadline. As a result, they do not scale well into hard real-time embedded environments that have much stricter per-iteration timing constraints, often in a 1 ms range. We found the following approach to work much better for real-time Tokamak Fusion Reactor Structured data flow programming environments: Compiled graphical development Divide algorithm into two steps: Target deskton, mobile, industrial, and embedded 1) Off-line calculation - it is acceptable for this step to be expensive, as it is done up front Thousands of out-of-the box mathematics and signal 2) On-line calculation - this step uses input and off-line calculation data to compute outputs deterministically processing routines $R\frac{\partial}{\partial R}\left(\frac{1}{R}\frac{\partial\psi}{\partial R}\right) + \left(\frac{\partial^2\psi}{\partial Z^2}\right) = -\mu_o RJ(\psi)$ Seamless connectivity to millions of I/O devices We applied this approach to matrix-vector multiplication and PDE solver problems that are at the heart of many Sing. control applications. PDEs are used to describe the system and matrix-vector multiplication is used to apply the model to sensor data in order to generate actuator data. Grad-Shafranov PDE Matrix-Vector Multiplication in real-time Custom FPGA-based Communication Protocol Magnetohydrodynamics Shape Control Application Layers Currently: 3-12 ms, 39x63 grid E-ELT Telescope Goal: 1ms Layer 1 Off-line calculations LahVIEW ESO (European Southern Observatory) Data distribution Geometry-aware algorithm: Five mirrors (M1 through M5) 1.Cast PDE into a set of equations: CPU affinity feature for cache control Mx - b = f(x)National Instruments involvement 2.Use iterative algorithm: $x^{(n+1)} = M^{-1}{f[x^{(n)}] + b}$ (keep fixed data in cache) Network topology: ring Data Acquisition 3.Reduce problem for each iteration by finding a set of points (pink) that, when Read sensor data, distribute across Network speed: up to 110 MB/s (cable dependent) M1 – 42m primary segmented mirror calculated, exactly split the problem into two or more smaller independent sub-problems CPUs (machines), Collate and update Configurable: RX and TX parameters, data sink, data M4 – adaptive mirror (blue and green) outputs source, data recombine Program FPGA 4.Calculate the exact solution for the points using corresponding rows of M Development time: 2 weeks 5.Repeat for each sub-problem Call di Throughput/Latency for 80MB/s network setup Calculating the exact solution for Graph representation E-ELT Telescope: M1 Mirror these arid points splits arid into two of solution grid points Packet length (f32s) Throughput (MB/s) First byte latency per node (µs) independent sections of smaller size Layer 2 C code 46 Problem. 984 hexagonal segments 8 1 For a 1000x1000 grid, M dimensions are Memory management 6 sensors/3 actuators per segment 67 15 data alignment M(1.000.000 x 1.000.000) 1 ms control cycle 65 2.5 Iterate over sub-problems Calculating M⁻¹ directly may be impossible 70 Math tricks to reduce problem to 3k x 3k Solution symmetric matrix by 3k vector multiplication Solve the problem for the unit boundary vector using any **FPGA Block Diagram** Layer 3 method (for example, iteration over smaller subsets of the New multiplication algorithm needed Assembly code problem for which M-1 can be calculated directly). Values for 750 us (worst case) Hand coded SSE2 assembly for solving Dell T7400 (2x2.6GHz 12MB L2 Quad PC the solution corresponding to the pink points represent sub-problems PC Memor elements in the M⁻¹ rows corresponding to the unit boundary Core Xeon) L2 & L1 cache optimized Matrix restructured off-line in L1/L2 cache vector. Repeat the process for all unit vectors DMA Math.dll optimized sub-problems and loaded into the Interaction between solution grid FPGA Grid points calculated CPU caches points (represents matrix M) with reduced set of GPU (dual Tesla): 850 us Minimum Grid Size boundary condition Custom communication protocol Layer 4 L2 Cache Threshold LabVEW on a Die Transmit CPU of-load for data recombination 111x63 grid (6993 non-linear equations with 6993 Matrix fits | Matrix too big Block Œ Block Fully hand-shaked, with retries unknowns) 0 12000 7th order RHS polynomial Slower nodes will gate data delivery Hold time f32 ADD Hold time 10000 DMA for data access directly from Packet Size One iteration: < 250 us Packet Size 8000 memory 10⁻⁵ error: 4 iterations starting from 0s 6000 Total time for solution: <1 ms 4000 Direction toggling for Dell 7400T (2x2.6GHz Quad Core Xeons) 2000 better I 2 cache utilitization 1000 2000 3000 4000 0 Linear PDE Benchmarks (µs) LabVIEW Targets Matrix Size rEdge 1955) Spectral New Method (CPUs) E-ELT Telescope: M4 Mirror 1 8 1 2 4 8 Problem Grid points 6000-9000 actuators calculated with full Grid 39 73 40 32 34 72 115 1ms control cycle Boundary set of boundary 63x63 495 96 84 100 66 61 85 Math: 9k x 15k matrix by 15k vector multiplication Conditions 2 x quad-com per blade 127x127 2130 363 128 381 211 157 129 Both CPU bandwidth and cache size limited Geometry-aware algorithm 355×355 10321 1498 296 6073 2866 2408 308 distributed computation required applied to a rectangular PDE grid 319x319 16594 2589 459 1416 Custom FPGA-based communication protocol 45748 10883 965 29195 15171 13873 11814 Multi-blade setup for distributed calculation 1 Deterministic communication 2.On-the-fly data recombination



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