

High Performance Parallel Implementation of Adaptive Beamforming Using Sinusoidal Dithers

High Performance Embedded Computing Workshop

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Overview



- Gradient Estimation Using Sinusoidal Dithers
- Application to Adaptive Beamforming
- Parallel Implementation
- Results
- Conclusion

Gradient Estimation Using Dithers



Consider any objective function f(x) where x is an N-by-1 vector.
For each component of x, superimpose a sinusoidal dither of different frequency, as in

$$x' = x + \theta = x + \alpha \left[\cos(\omega_1 t), \cos(\omega_2 t), \dots, \cos(\omega_N t) \right]^T$$

where α is a small scalar. The Taylor series expansion of $f(x)$ yields.

$$f(\mathbf{x}') = f(\mathbf{x} + \boldsymbol{\theta}) = f(\mathbf{x}) + \nabla f(\mathbf{x})^T \boldsymbol{\theta} + \frac{1}{2} \boldsymbol{\theta}^T \nabla^2 f(\mathbf{x}) \boldsymbol{\theta} + \dots$$
$$= f(\mathbf{x}) + \alpha \sum_{i=1}^N \frac{\partial f}{\partial x_i} \bigg|_{\mathbf{x}} \cos(\omega_i t) + \dots$$

Gradient Estimation Using Dithers - 2



• The components of the gradient vector can be determined exactly after we multiply f(x') by $\cos(\omega_j t)$, for j = 1, 2, ..., N. The result after using trigonometric identities is,

$$f(\mathbf{x}')\cos(\omega_{j}t) = f(\mathbf{x})\cos(\omega_{j}t) + \frac{\alpha}{2}\frac{\partial f}{\partial x_{j}}\Big|_{\mathbf{x}}$$
$$+ \frac{\alpha}{2}\frac{\partial f}{\partial x_{j}}\Big|_{\mathbf{x}}\cos(2\omega_{j}t) + \frac{\alpha}{2}\sum_{i\neq j}\frac{\partial f}{\partial x_{i}}\Big|_{\mathbf{x}}\cos(\left[\omega_{i} - \omega_{j}\right]t)$$
$$+ \frac{\alpha}{2}\sum_{i\neq j}\frac{\partial f}{\partial x_{i}}\Big|_{\mathbf{x}}\cos(\left[\omega_{i} + \omega_{j}\right]t) + H.O.T.$$

Note that the only constant term on the right hand side is the *j*th component of the gradient vector scaled by the factor α/2, and can be recovered exactly by low-pass filtering f(x')cos(ω_it).

Dither Application



- Dithers can be applied sequentially or in parallel
- If applied in parallel, care must be taken to avoid crosstalk between adjacent channels which may corrupt the gradient estimate
 - Requires high sampling rate, and narrow low pass filter with steep transition regions

Normalized Low Pass Filter Response (red) and Dither Locations (blue) -8 Power - dB -10 -12 -14 -16 -18 -20 -0.5 -0.3 -0.2 0.2 -0.4 -0.1 0 0.1 0.3 0.4 0.5 $f = \omega/2\pi$

Advantages of Using Dithers in Adaptive Beamforming



PROs

- Estimates gradient exactly and results in higher SINR than other recursive algorithms
- Very scalable algorithm
 - Each dither computation is independent of others and can therefore be distributed across parallel processors

CONs

 Increased computations and FLOP count

Brief Overview of Adaptive Beamforming



- Linearly Constrained Minimum Variance (LCMV) Beamformer
- Minimize output power of an array, y(n) = w^Hu(n), subject to a set of linear constraints

minimize
$$J(n) = E\left[\left|y(n)\right|^2\right]$$

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such that C^H w = f.
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Decompose w into,

$$w = w_q - Bw_a$$

 $y(n) = w_q^H u(n) - w_a^H B^H u(n).$

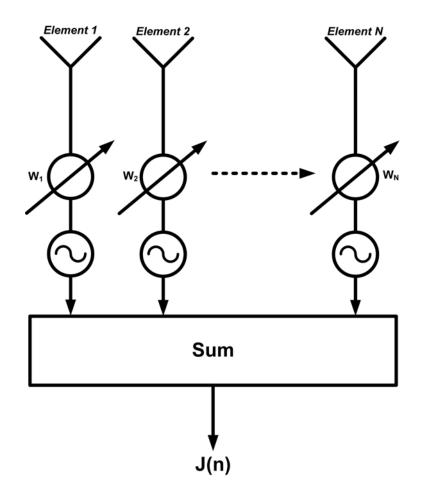
Rewrite as unconstrained program,

$$\begin{array}{ll} \underset{w_{a}}{\text{minimize}} & J(n) \\ \boldsymbol{w}_{a}(n+1) = \boldsymbol{w}_{a}(n) - \mu \nabla_{\boldsymbol{w}_{a}} J(n). \end{array}$$

Adaptive Array Configuration With Dithers

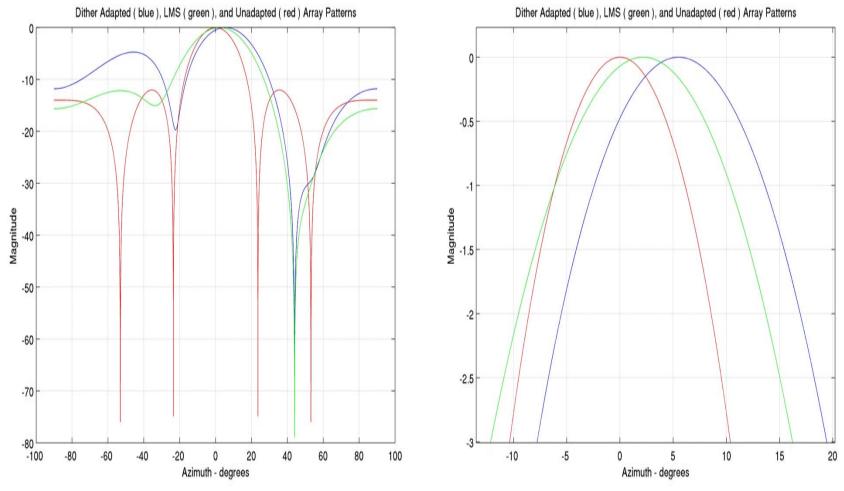


 Separate dither must be applied to real and imaginary part of each array element weight



Adaptive Array Performance

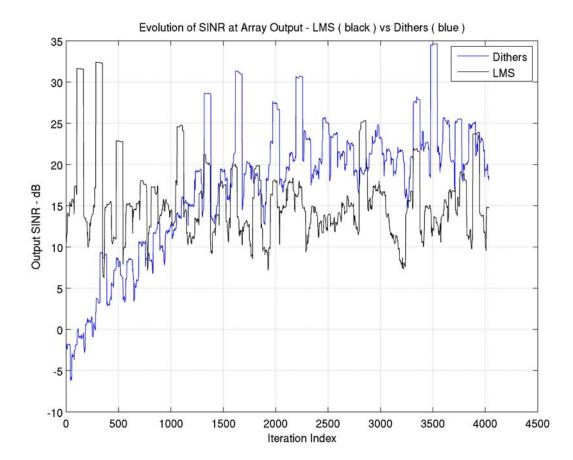




Jammer at 44° azimuth

Adaptive Array Performance - 2

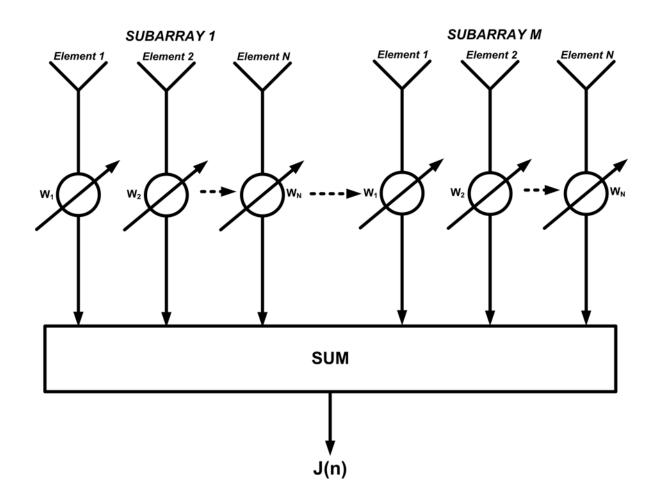




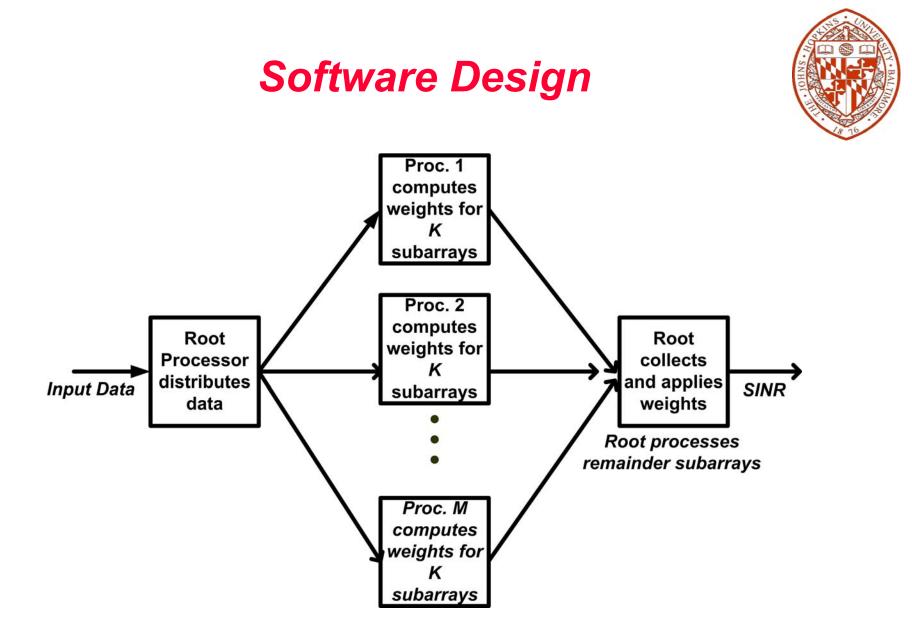
10

Subarray Architecture





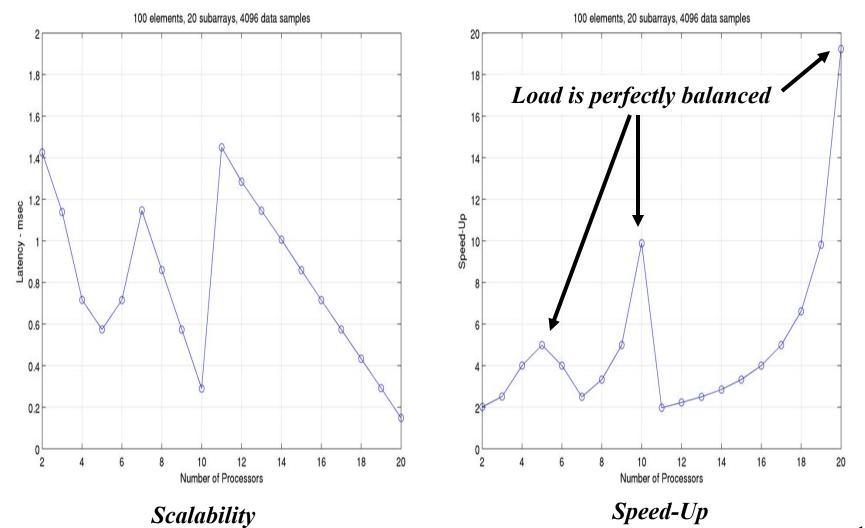
For this study, techniques to minimize grating lobes were not considered



MPI Implementation on Cray XD1

Measured Results





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Conclusions



- Directly estimating the components of the gradient vector using sinusoidal dithers may improve the performance of steepest descent algorithms
 - Adaptive beamforming
- Subarray implementation of adaptive beamformer evaluated on Cray XD1 using parallel processors
 - Convolution (dot product) and mixing operations may be implemented on FPGAs for further performance improvements