

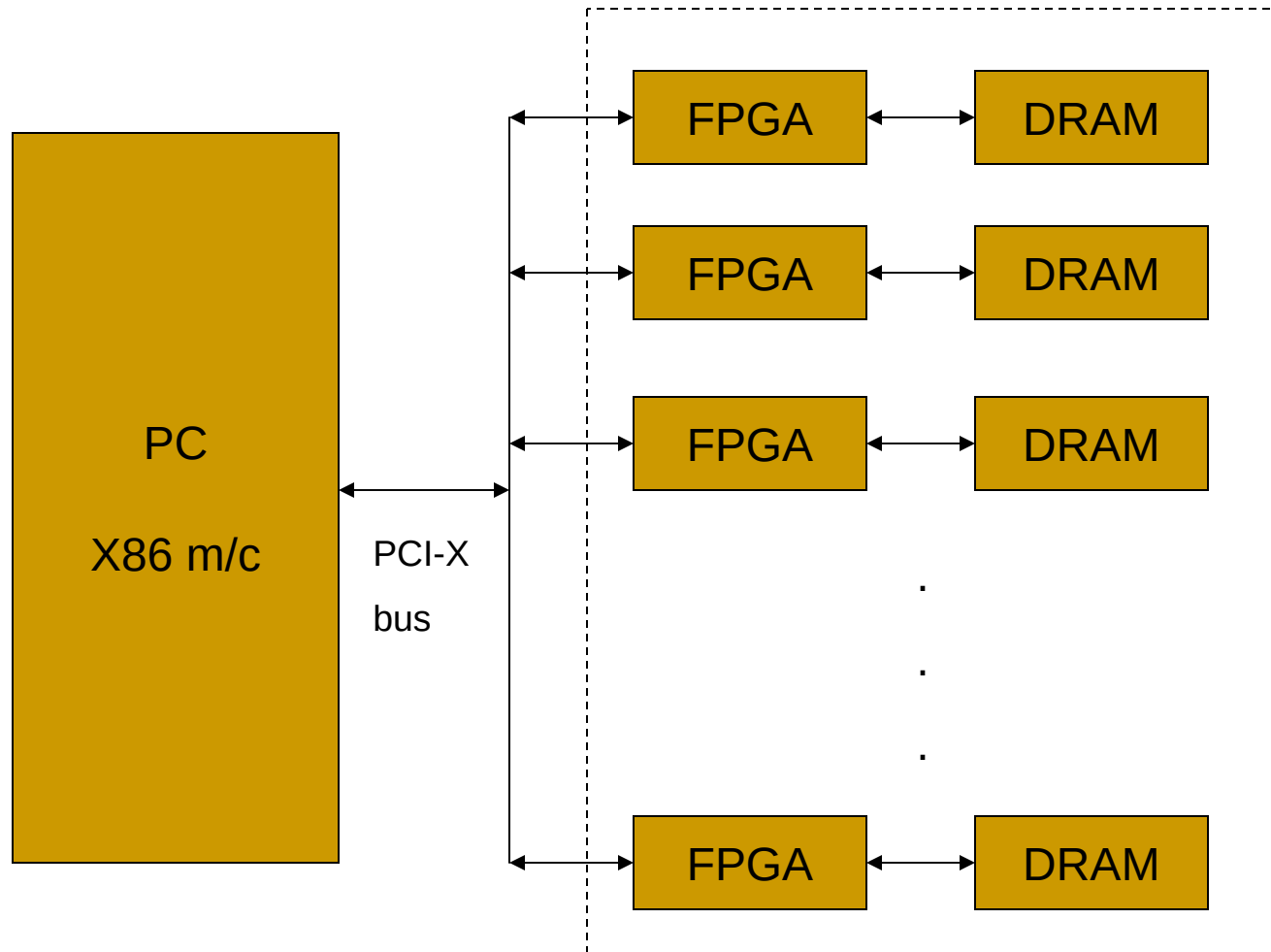
Evaluation of mapping the LU decomposition Algorithm on a multi-FPGA system (Starbridge HC-36)

Arvind Sudarsanam

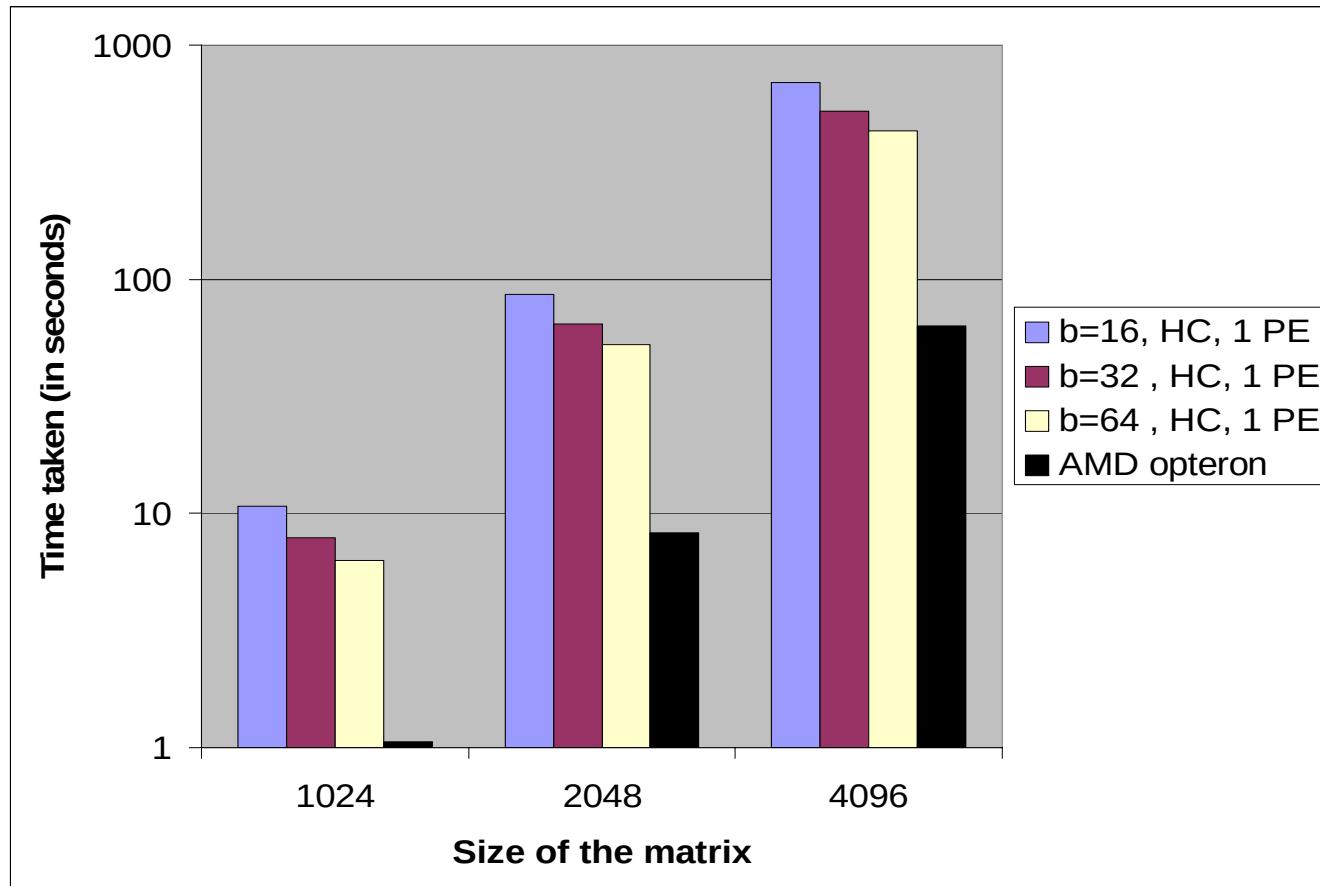
Aravind Dasu

Thomas Hauser

Block-based LU on Hyper-computer



Projected time taken for step-4 on FPGA versus Observed time on AMD opteron



NOTE: Step-4 is $O(N)$ times more complex than steps 2 and 3