

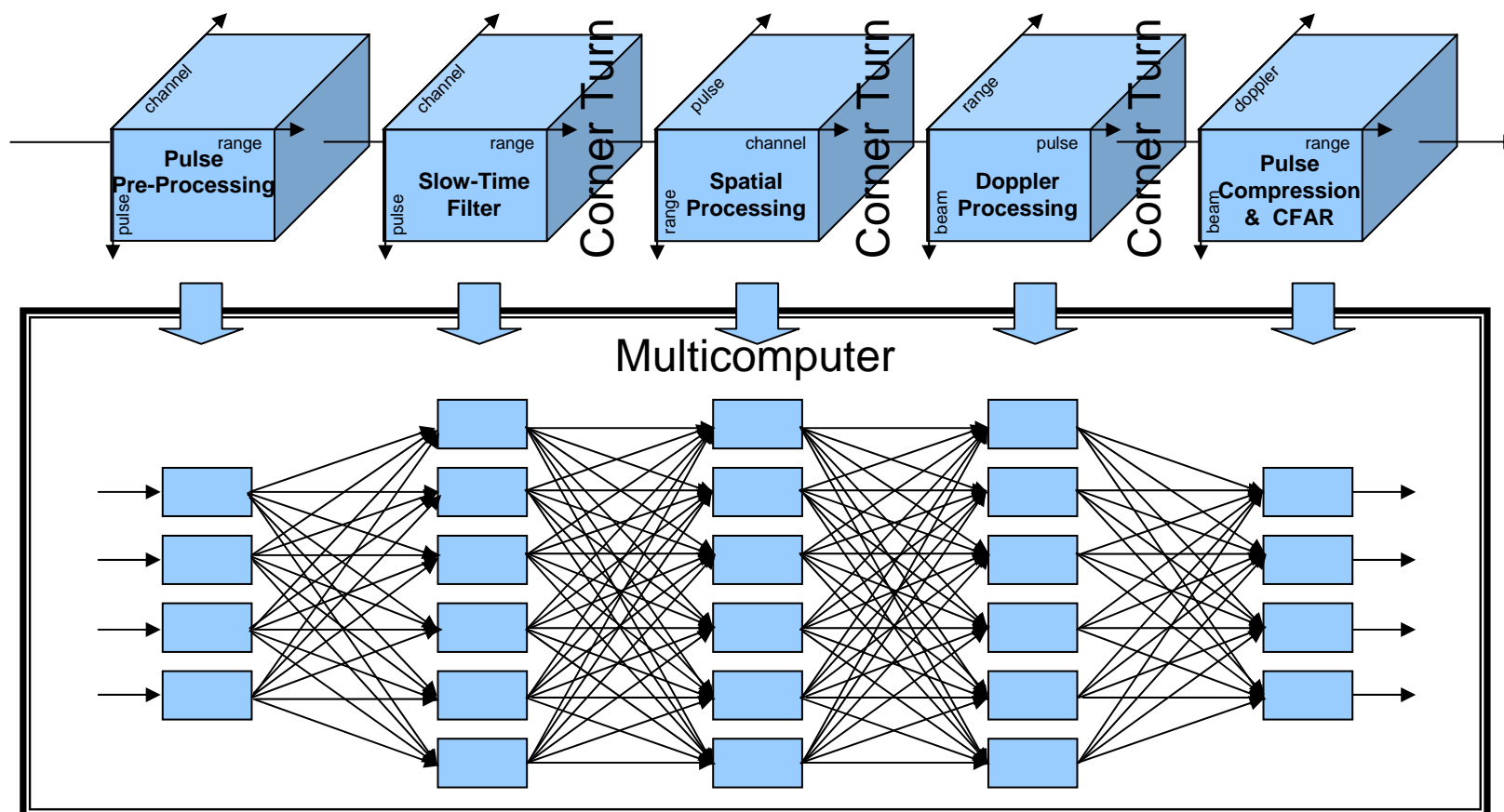
Scalable Software Interconnect for Distributed Radar Signal Processing

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Problem: Multicomputer Radar System

- **Throughput and latency achieved through pipelined SPMD architecture**

- Memory access efficiency achieved by data reorganization at global and local levels
- Concurrent processing and inter-stage data-reorganization achieved through DMA
- Processing efficiency achieved through platform optimized function library



Solution: Distributed Dataflow Management

- Use a standardized framework and terminology

- Manage complex data reorganization and redistribution on a distributed memory system
- Achieve concurrent reorganization, redistribution, communication, and processing
- Access memory efficiently for high throughput
- Achieve application an performance scalability

