

Accelerating Blocked Matrix-Matrix Multiplication using a Software-Managed Memory Hierarchy with DMA

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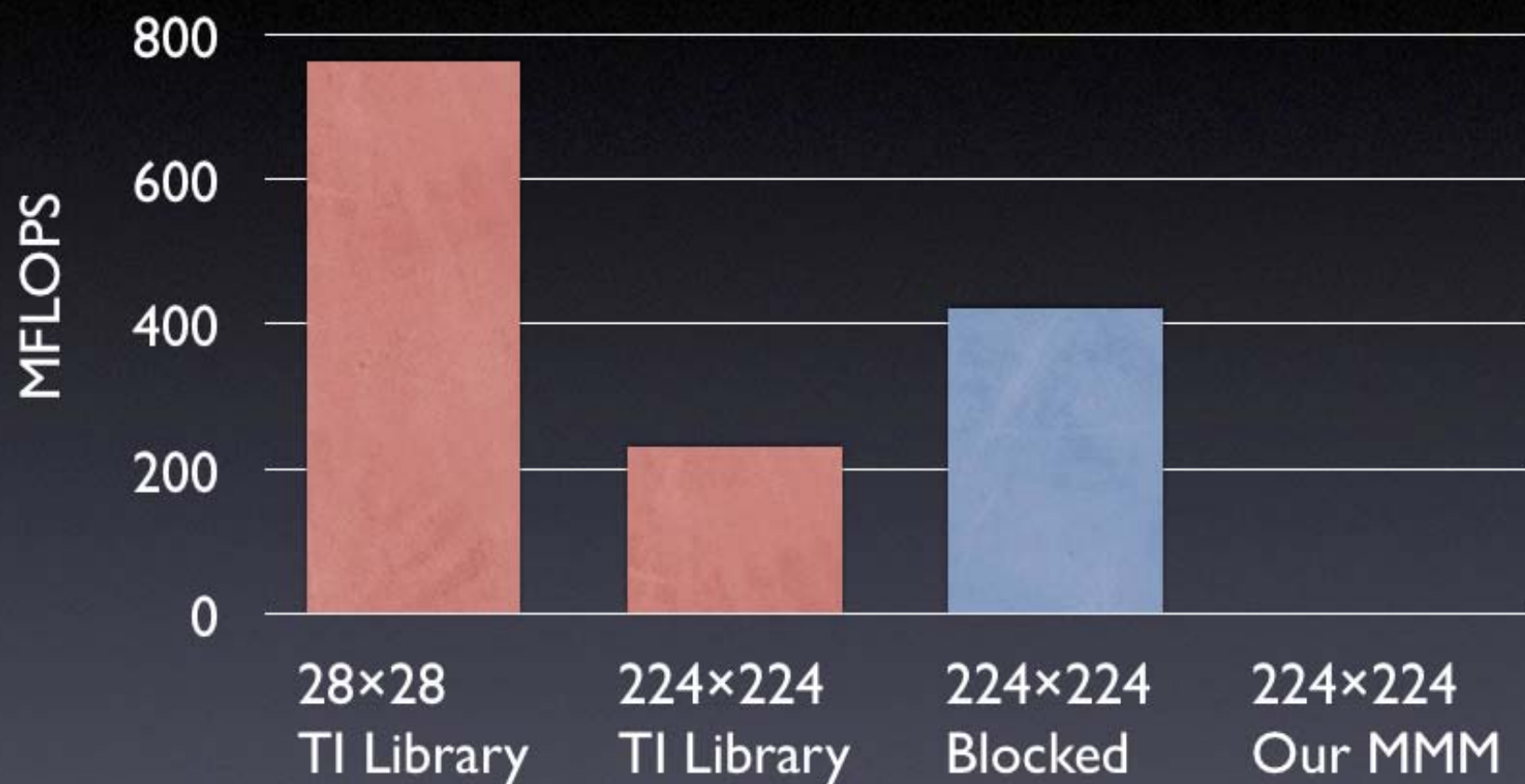
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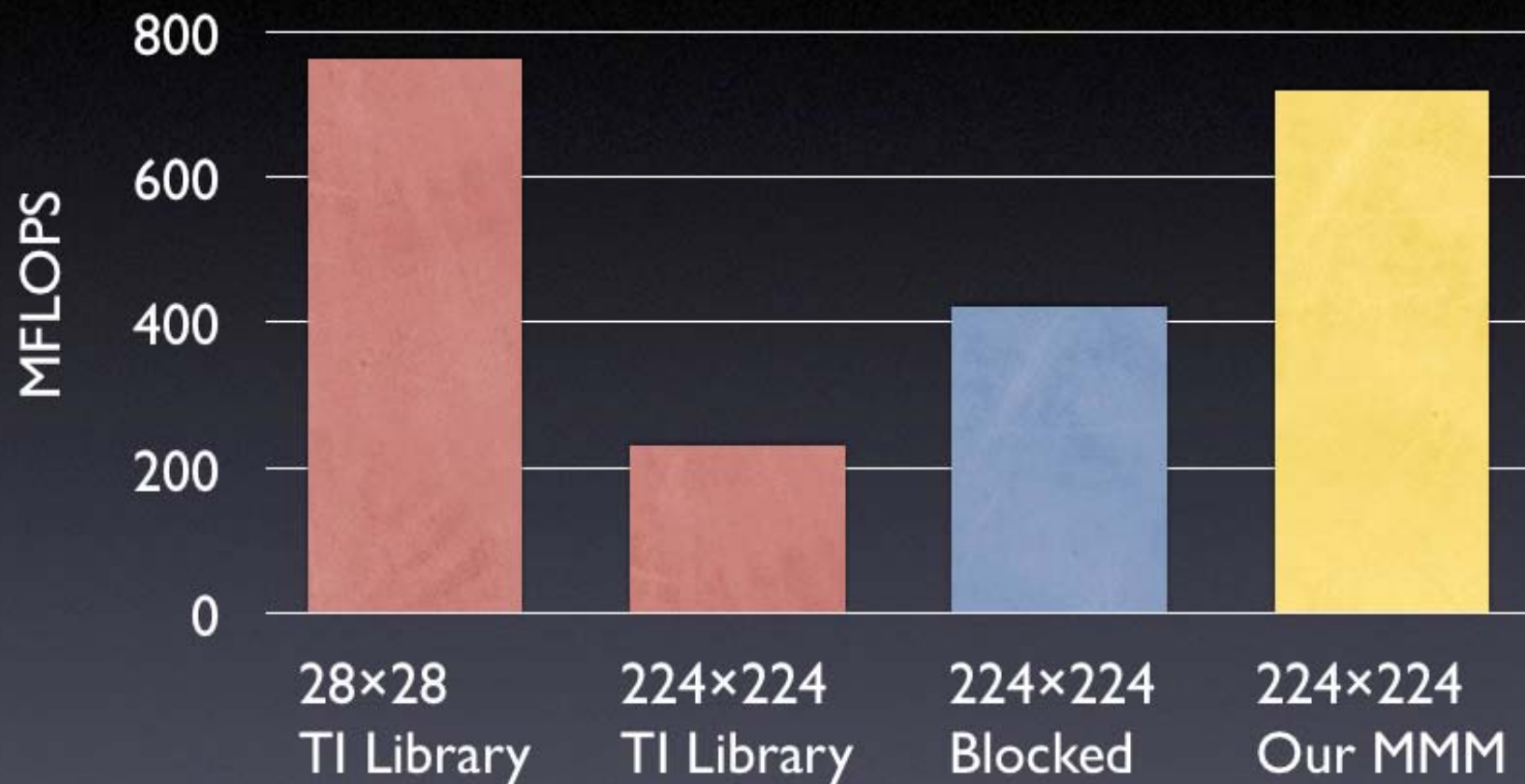
MMM on the TI 6713 DSP



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Our MMM uses scratch pad memory + DMA

Achieves in-cache performance for large matrices