Software Architectures for Morphing in Polymorphous Computing Architectures

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High Performance Embedded Computing Workshop 28 September 2004







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Morphing in PCA Architectures

- The DARPA Polymorphous Computing Architectures (PCA) program is developing embedded highperformance computing platforms with strong, rapid reconfigurability
- PCA processors are essentially "multiprocessors on a chip"
 - □ tiled architectures

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- reconfigurable processing aggregates
- reconfigurable networks
- "Morphing" is the reconfiguration and re-allocation of PCA hardware resources within a chip in response to various events
 - key capability to achieve PCA goals
 - portability across PCA chips must be maintained



Reconfigurable cache







PCA Two-Level Module Compilation Architecture

Two-level compile + customizable machine models enables targeting of same functionality to multiple machine configurations



PCA Morph Taxonomy Dimensions

A ta	axonon	ny of morph possibilities has				
bee		olished	byothe			Components change
FOr	UM Type 0a	Туре 1а	Туре 2а	Туре За	Type 4a	Туре 5а
allocation doesn't change	rphs di nitiator: ompiler	stingui applicat -genera	shed b tion, rur ted code	Application Vakes API call to change processing TEIME so within existing resource set.	Compiler instructions y Stem e, C allocated resources.	Compiler switches to a different library able to use the same resources.
	Resource	e consta	incy:_sta	atic, or (changed	Type 5b
Resource allocation changes	Component eplaced allocation of a running application transparently to the application.	eniturpers system configures resources and loads components at application startup.	Application makes API call to give up or gain some resources.	Contin makes API call to add or replace one or more components using different resources.	Ues por requests different resources to meet change in performance specified by metadata.	S Compiler switches to a different library that uses different resources.





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