



Dr. David Scott is the Petascale Product Line Architect for Intel Corporation. He is responsible for formulating and driving Intel strategy for developing the technology needed for high end computing, including processors, platforms, interconnect, and software. He is Intel's interface to the programs around the world that are developing systems that scale to a Petaflop and beyond.

Dr. Scott joined Intel in 1985, developing algorithms and math libraries for a sequence of distributed memory message passing computers, culminating in the first teraflop on the Linpack benchmark in 1996. He worked for five years in the Floating Point Center of Expertise, helping to improve floating-point design and validation for all of Intel's 32 and 64-bit processor projects. He spent three years in Singapore supporting High Performance Computing in the Asia-Pacific region, before returning to Oregon to take his current position in July 2006.

David received his PhD in Numerical Analysis from UC Berkeley in 1978. He was a research scientist at the Oak Ridge National Lab for 3 years and taught in the Computer Sciences department of the University of Texas for four years before joining Intel. While working for Intel, he received his MBA from the University of Oregon in 1990.