

General Chairman

Mr. Robert Bond / MIT Lincoln Laboratory

Technical Chairman

Dr. Jeremy Kepner / MIT Lincoln Laboratory

Advisory Committee

Dr. Richard Games / The MITRE Corporation

Mr. David Martinez / MIT Lincoln Laboratory

Sponsors

Dr. Dennis Healy / DARPA MTO

Dr. William Harrod / DARPA IPTO

Mr. Cray Henry / High Performance Computing
Modernization Program

Dr. Richard Linderman / Air Force Research Laboratory
Missile Defense Agency

Keynote Speaker

Dr. Michael McGrath / Department of the Navy,
Research, Development, & Acquisition

Prof. Jack Dongarra / University of Tennessee, ICL

Banquet Speaker

TBA

Invited Speakers

Prof. Anant Agarwal / Massachusetts Institute
of Technology CSAIL

Prof. Saman Amarasinghe / Massachusetts Institute
of Technology

Dr. Matteo Frigo / Cilk Arts, Inc.

Dr. James Held / Intel Corporation

Mr. Zachary Lemnios / MIT Lincoln Laboratory

Mr. Markus Levy / The Multicore Association &

The Embedded Microprocessor Benchmark Consortium

Dr. Norman Rubin / ATI Research

Dr. John Shalf / Lawrence Berkeley National Laboratory

Dr. Stephen Wheat / Intel Corporation

Dr. Hans P. Zima / Jet Propulsion Laboratory

Technical Committee

Dr. Stanley Ahalt / Ohio Supercomputer Center

Dr. James C. Anderson / MIT Lincoln Laboratory

Dr. Masahiro Arakawa / MIT Lincoln Laboratory

Dr. Ray Artz / Lockheed Martin Corporation

Dr. David A. Bader / Georgia Institute of Technology

Mr. William Bent, Jr. / CSP, Inc.

Dr. Lawrence Bergman / Jet Propulsion Laboratory

Mr. Robert Bernecky / Naval Undersea Warfare Center

Dr. Jay Brockman / University of Notre Dame

Dr. Keith Bromley / SPAWAR Systems Center

Mr. David Cousins / BBN Technologies

Prof. Jack Dongarra / University of Tennessee, ICL

Dr. Neall Doren / Sandia National Laboratories

Dr. Alan Edelman / Massachusetts Institute of Technology

Dr. Maya Gokhale / Los Alamos National Laboratory

Mr. Robert Gold / Office of the Undersecretary of Defense

Mr. John Grosh / Lawrence Livermore National Laboratory

Mr. Michael Harris / BAE Systems

Dr. William Harrod / DARPA / IPTO

Dr. Martin Herbordt / Boston University

Dr. Daniel Katz / Louisiana State University

Mr. Bruce Kinney / Raytheon Company

Dr. James Lebak / The MathWorks

Dr. Miriam Leaser / Northeastern University

Dr. Richard Linderman / Air Force Research Laboratory

Mr. Michael Lucas / Northrop Grumman Corporation

Mr. Craig Lund / Mercury Computer Systems, Inc.

Dr. Elias Manolakos / Northeastern University

Mr. Michael McCollister / Northrop Grumman Corporation

Ms. Janice McMahon / University of Southern California

Dr. Brent Nelson / Brigham Young University

Mr. David Ngo / BAE Systems

Mr. Eric Pancoast / Lockheed Martin Corporation

Dr. Robert Pettit / The Aerospace Corporation

Mr. Frank Pietryka / Hewlett Packard

Dr. Viktor Prasanna / University of Southern California

Dr. Albert Reuther / MIT Lincoln Laboratory

Dr. John Reynders / Eli Lilly

Dr. Mark Richards / Georgia Institute of Technology

Dr. Martin Rinard / Massachusetts Institute
of Technology CSAIL

Dr. Sharon Sacco / MIT Lincoln Laboratory

Mr. Craig Sather / The Aerospace Corporation

Dr. Phil Sementilli / Raytheon Company

Dr. Gary Shaw / MIT Lincoln Laboratory

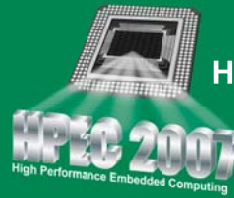
Dr. Anthony Skjellum / Verari Systems Software, Inc.

Dr. Hendrik Spaanenburg / Advanced Principles Group, Inc.

Mr. Brian Sroka / The MITRE Corporation

Dr. Jeffrey Vetter / Oak Ridge National Laboratory

Mr. Daniel Wells / The Boeing Company



Eleventh Annual Workshop on

High Performance Embedded Computing

18–20 September 2007

Massachusetts Institute of Technology
Lincoln Laboratory

Call for Presentations

The High Performance Embedded Computing (HPEC) technical committee announces the Eleventh Annual HPEC workshop to be held 18–20 September 2007 at MIT Lincoln Laboratory in Lexington, MA. The workshop will give U.S. government-funded researchers from academia, industry and government, working in this important area, an opportunity to discuss techniques, approaches and ongoing developments with relevance to real-time embedded computing and related military applications. During this eleventh year, the HPEC 2007 workshop will have as its theme “Multicore processors and their impact on DoD HPEC systems.” The technical committee seeks new presentations that clearly describe advances in high performance embedded computing technologies, emphasizing one or more of the following topics:

- Automated Design Tools
- Advanced Middleware and Software Technology
- Mapping and Scheduling of Parallel and Real-Time Applications
- Case Studies and Benchmarking of HPEC Applications
- Signal Processing Techniques
- Computing Technologies for Challenging Form Factors
- ASIC and FPGA Advances
- Parallel and Distributed System Architectures
- Networking and Interconnects
- Fault-Tolerant Computing
- Digital Front-Ends
- Technology Trends

All submittals will be competitively reviewed and judged on their technical merit, novelty and relevance to the technical program. Preference will be given to papers with strong, quantitative results. The workshop will have both Open Sessions and Unclassified/U.S. Only sessions, as well as poster presentations. Presenters with significant hardware demonstrations are encouraged to apply to the poster sessions. Since the presentations themselves will constitute the published proceedings, speakers are requested to prepare charts of the highest quality. Electronic copies of the charts presented at the workshop will be included in the workshop proceedings.

Authors are invited to submit two-page extended abstracts via our web-site:

<http://www.ll.mit.edu/HPEC>

For further details about the workshop, please contact

Holly Fawcett
MIT Lincoln Laboratory
244 Wood Street, J-217C
Lexington, MA 02420
Phone: (781) 981-4842
Email: hpec@ll.mit.edu

Important Dates: Abstract submission deadline
Notification of acceptances

21 May 2007
20 July 2007



HPCMP